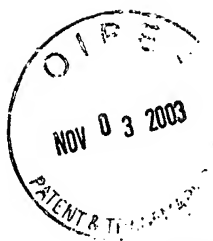


PATENTS
LT-158 CON



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant : Steven D. Roach
Application No. : 10/613,420 Confirmation No. 5157
Filed : July 03, 2003
For : ULTRA-WIDEBAND CONSTANT GAIN CMOS
AMPLIFIER
Group Art Unit : 2817
Examiner : Not yet assigned

New York, New York 10020
October 29, 2003

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, applicant hereby makes the documents listed below of record in the above-identified application.

U.S. Patent Documents

Chin et al.	3,486,126	12/23/1969
Evel	3,573,644	04/06/1971
Garcia et al.	3,717,820	02/20/1973
Atkinson	3,769,604	10/30/1973
Sechi	4,409,557	10/11/1983

Roach	5,121,075	06/09/1992
Samay et al.	5,276,406	01/04/1994
Ebihara et al.	6,249,186	06/19/2001
Yuan	6,392,492	05/21/2002
Moore et al.	6,511,432	01/28/2003

Other Documents

Addis, John, "Three technologies on one chip make a broadband amplifier", Electronics The International Magazine of Electronics Technology, June 5, 1972, pp. 103-107.

Ahuja, B.K., "Implementation of Active Distributed RC Anti-Aliasing/Smoothing Filters", IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 6, pp. 339-342.

AMCO Product Brief 1.0/1.25GBPSVCSELDRIIVER, May 10, 1999, pp. 1-3.

Dascher, David J., "Measuring Parasitic Capacitance and Inductance Using TDR", Hewlett-Packard Journal, April 1996, pp. 1-19.

Ikalainen, Pertti, K. "An RLC Matching Network and Application in 1-20 GHZ Monolithic Amplifier", IEEE MTT-S International Microwave Symposium Digest, Vol. I, 1989, pp. 1115-1118.

Khoury, John, M., "Synthesis of Arbitrary Rational Transfer Functions Using Uniform Distributed RC Active Circuits", IEEE Transactions on Circuits and Systems, Vol. 37, No. 4, April 1990, pp. 464-472.

Khoury, John, M., "On the Design of Constant Settling Time AGC Circuits", IEEE Transactions on Circuits and Systems, Vol. 45, No. 3, March 1998, pp. 283-294.

MAXIUM, "Interfacing Maxim Laser Drivers with Laser Diodes", May 2000, pp. 1-12.

Sackinger, Eduard, et al., "A 3GHz, 32dB CMOS Limiting Amplifier for SONET OC-48 Receivers", IEEE International Solid-State Circuits Conference, 2000, pg. 158.

SUMMIT Microelectronics, Inc. "Dual Loop Laser Diode Adaptive Power Controller with Look Up Table" (SML2108), October 3, 2001, pp. 1-21.

Swartz, R.G. et al., "An Integrated Circuit for Multiplexing and Driving Injection Lasers", IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 4, August 1982, pp. 753-760.

The references listed above are also listed on the attached Form PTO-1449 (submitted in duplicate). Pursuant to 37 C.F.R. § 1.98(d), applicant is not submitting copies of these references listed above because they were previously cited by or submitted to the Office in parent application No. 10/172,874, filed June 13, 2002, from which priority is claimed. However, if it would assist the Examiner, applicant will furnish copies of any or all the above cited references upon request by the Examiner.

It is respectfully requested that the above-listed documents be fully considered by the Patent and Trademark Office during the examination of this application. Applicants request that a copy of Form PTO-1449, as considered and initialed by the Examiner, be returned with the next communication.

This Information Disclosure Statement is being filed more than three months from the application filing date but before the mailing date of the first Office Action on the merits. Pursuant to 37 C.F.R. § 1.97, submission of this Statement requires no fee. However, if for any reason a fee is due, the Director of the U.S. Patent and Trademark Office is hereby authorized to charge payment of any fees

required in connection with this Information Disclosure Statement to Deposit Account No. 06-1075. A duplicate copy of this Information Disclosure Statement is transmitted herewith.

An early and favorable action is respectfully requested.

Respectfully submitted,

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I hereby certify that the
information furnished
is true and correct
and that I am duly
qualified to act as
agent for the
applicant.
P.O. Box 6
Alexandria, VA 22303-1460 on

Claire J. Saint-van Goodman
29, Jan 83
Signature of Person Signing

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
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STATEMENT BY APPLICANTAPPLICANT
Steven D. RoachCONFIRMATION NO.
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,486,126	12/23/1969	Chin et al.			
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

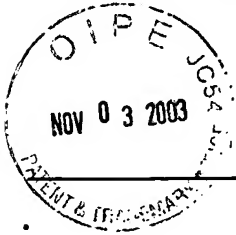
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